

Amendments to the Specification:

Please replace the paragraph beginning at page 5, line 19 with the following amended paragraph:

A similar process is used in the receive direction. A single ADC 14 digitizes a frequency-multiplexed voice and data signal 40, converting the analog signal to a digital signal 41. The digital signal 41 is sent to an ADSL ATU-C Receiver where the voice component is filtered from the signal and the remaining data signal is processed. The ADC output 41 is also sent to a re-timer ~~36~~ 37. The re-timer ~~36~~ 37 operates in a similar fashion to that previously described. In this instance, however, the frequency is being changed from $8 \cdot N$ kHz to $(8 + \delta) \cdot N$ kHz instead of from $(8 + \delta) \cdot N$ kHz to $8 \cdot N$ kHz.

Please add the following new paragraph to the specification at page 7 after line 9.

Also shown in Fig. 3 is a multiplexer circuit 70 coupled to multiplex said data signal with the adjusted voice signal for transmission and a demultiplexer circuit 72 coupled to demultiplex said data signal with the adjusted voice signal for transmission.